

Dated: August 13, 2026

NOTICE
Minors in Electronics Engineering (VLSI Design)

The following Electronics based courses shall be offered as **optional** courses to all students of Non-Electronics branches admitted in session 2025-2026 and 2024-2025 (presently in 3rd or 5th semester). The courses shall be offered preferably during the evening hours of the regular working days or over the weekends. The students who will complete six courses as per the following table will be awarded a certificate of “**Minor in Electronics Engineering (VLSI Design)**” upon completion.

Details of Courses		L	T	P	Cr.
Third Semester					
U_1	ELECTRONIC DEVICES FOR INTEGRATED CIRCUITS	3	0	2	4.0
U_2	DIGITAL SYSTEM DESIGN	3	0	2	4.0
Fourth Semester					
U_3	CMOS DIGITAL INTEGRATED CIRCUITS	3	0	2	4.0
U_4	ANALOG CMOS DESIGN	3	0	2	4.0
Fifth Semester					
U_5	VLSI VERIFICATION AND TESTING	3	0	2	4.0
U_6	PHYSICAL DESIGN AUTOMATION	3	0	2	4.0

An amount of Rs. 10,000/- per course (Maximum 2 in each semester) would be charged additionally from the student under the Minor Courses Module scheme, except for courses which shall be offered in the regular curriculum.

Note:

1. In case any of the above mentioned courses, is in the regular scheme of courses as a ‘compulsory course’ of any branch, then the students of that branch need not study that course in “Minors in EE (VLSI Design)”.
2. In case any of the above mentioned courses, is in the regular scheme of courses as an ‘Elective Course’ of any branch, then the students of that branch will have to study that course in Minors in EE(VLSI Design). The student will have to study some other course as an elective (other than this course) to be considered for completion of credits for his branch.
3. The students in ECE or related branches i.e., ECE, ENC and EVD are not eligible for ‘Minors in EE(VLSI Design)’.

Please send duly filled application form (available with this notice) with fee receipt to rupinderpal.singh@thapar.edu by **August 22nd, 2026**. Classes for ‘Minor in EE (VLSI Design)’ will start from **August 24th, 2026 (Monday)**.

THAPAR INSTITUTE OF ENGINEERING AND TECHNOLOGY
(Deemed-to-be-University)

APPLICATION FORM FOR MINOR IN EE(VLSI DESIGN)

Part - A: To be filled up by the student

1.	Name of Student		
2.	Regn /Roll No.		
3.	Program and Branch		
4.	C.G.P.A		
5.	Mobile No.		
6.	Details of Fee paid	Rs..... Dated.....	Receipt No.....
7.	Course(s) opted	Course No.	Course Title

Signature of the student

Date: _____

Part - B (for office use)

Verified, the student has opted for minor in EE(VLSI Design) and paid the requisite fee.

Approved
(Dean, Academic Affairs)

The students can also make the fee payment using any one of the following modes:

- i) Online using your Debit Card/Credit Card/Net Banking by visiting the link
<https://eazypay.icicibank.com/eazypayLink?P1=/QXDwbDYmH8fJlaL4JBpww==>
- ii) NEFT /RTGS to following bank account:
Beneficiary Name: Thapar Institute of Engineering & Technology
Bank Account Number: 65181840370
IFSC: SBIN0050244

Submit the duly filled form (soft copy) along with fee receipt @ Rs 10,000/- per course on or before 22nd August, 2026 at rupinderpal.singh@thapar.edu